

S/N 10/642,702

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors:	Vivek Jaiswal et al.	Examiner:	Chirag R. Patel
Assignee:	Intel Corporation	Confirmation No.:	4365
Serial No.:	10/642,702	Group Art Unit:	2454
Filed:	August 18, 2003	Docket No.:	P16507
Title:	DYNAMIC LOAD DISTRIBUTION WITHIN A SESSION INITIATION PROTOCOL NETWORK		

AMENDMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

This is in response to the Office Action mailed April 12, 2010. Reconsideration and allowance of the subject application, as amended, are respectfully requested. Please amend the subject application as follows: